

ABSTRACT OF THE DISCLOSURE

A protection circuit, to be provided for a circuit arrangement having an inductive load and an FET as an N-channel
5 MOS transistor provided upstream of the load with respect to a flow of power current, the FET controlling an energization state of the load, the protection circuit includes a first connection changer interposed on a connection line between a gate of the FET and a gate drive voltage supply source, the
10 first connection changer changing a connection state between a first connection state in which the gate is connected to the gate drive voltage supply and a second connection state in which the gate is connected to a ground.

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